Title: **Automatic Detection of Alignment or Registration Marks**

U.S. Patent References:

1. U.S. Patent 5,315,700 entitled, "Method and Apparatus for Rapidly Processing Data Sequences", by Johnston et. al., May 24, 1994

2. U.S. Patent 6,130,967 entitled, "Method and Apparatus for a Reduced Instruction Set Architecture for Multidimensional Image Processing", by Shih-Jong J. Lee, et. al., October 10, 2000

3. Pending Application Ser. No. 08/888,116 entitled, "Method and Apparatus for Semiconductor Wafer and LCD Inspection Using Multidimensional Image Decomposition and Synthesis", by Shih-Jong J. Lee, et. al., filed July 3, 1997

4. U.S. Patent No. 6,122,397 entitled, "Method and Apparatus for Maskless Semiconductor and Liquid Crystal Display Inspection", by Shih-Jong J. Lee, et. al., September 19, 2000

5. U.S. Patent No. 6,148,099 entitled, "Method and Apparatus for Incremental Concurrent Learning in Automatic Semiconductor Wafer and Liquid Crystal Display Defect Classification", by Shih-Jong J. Lee et. al., November 14, 2000

6. U.S. Patent No. 6,141,464 entitled, "Robust Method for Finding Registration Marker Positions", by Handley; John C, issued October 31, 2000.

Co-Pending U.S Patent Applications

1. U.S. Patent Application Ser. No. 09/693723, "Image Processing System with Enhanced Processing and Memory Management", by Shih-Jong J. Lee et. al., filed October 20, 2000

2. U.S. Patent Application Ser. No. 09/693378, "Image Processing Apparatus Using a Cascade of Poly-Point Operations", by Shih-Jong J. Lee, filed October 20, 2000

3. U.S. Patent Application Ser. No. 09/692948, "High Speed Image Processing Apparatus Using a Cascade of Elongated Filters Programmed in a Computer", by Shih-Jong J. Lee et. al., filed October 20, 2000

4. U.S. Patent Application Ser. No. 09/703018, "Automatic Referencing for Computer Vision Applications", by Shih-Jong J. Lee et. al., filed October 31, 2000

5. U.S. Patent Application Ser. No. 09/702629, "Run-Length Based Image Processing Programmed in a Computer", by Shih-Jong J. Lee, filed October 31, 2000

6. U.S. Patent Application Ser. No. 09/738846 entitled, "Structure-guided Image Processing and Image Feature Enhancement" by Shih-Jong J. Lee, filed December 15, 2000.

7. U.S. Patent Application Ser. No. 09/739084 entitled, "Structure Guided Image Measurement Method", by Shih-Jong J. Lee et. al., filed December 14, 2000.

References:

1. Serra, Fernando J., "Advanced Search Techniques for Alignment and Registration", Intelligent Vision '99, June 28-29, 1999

Technical Field

The invention is related to image processing and pattern recognition and, more particularly, to detecting and classifying alignment or registration mark type and measuring the position and orientation of a mark.

Background of the Invention

In the semiconductor wafer production process and multilayer electronic circuit board construction, numerous individual processes are performed sequentially to construct layers of a three dimensional electronic circuit. The general process depends critically on the alignment of each of the individual processes. To characterize alignment between layers, image primitives called alignment or registration marks are imprinted during each process step. By measuring the relative positions of these registration marks, the registration of layers can be determined. Layers can be mis-registered in x and y position and the two layers can be rotated with respect to each other. The amount of mis-registration that is allowable depends upon the application and the critical dimensions of the electronic circuit that is being constructed. Mis-registration detection is important because of its effects on yield and performance of the finished circuit.

Detection of alignment or registration marks and their accurate characterization may be done manually or automatically. Both processes suffer from corruption of the marks by noise and processing artifacts that cause interference with the basic imprinted marks. Interference creates gray scale patterns that perturb the simple patterns and the background, making it more difficult to measure basic registration information.

Prior Art

An image segmentation approach is used in the prior art for image feature detection or object measurement. The image segmentation approach converts a grayscale image into a binary image that contains object of interest masks. Binary thresholding is a common technique in the image segmentation approach (U.S. Patent No. 6,141,464 entitled, "Robust Method for Finding Registration Marker Positions", by Handley; John C, issued October 31, 2000 column 4 lines 58-59).

Image features such as edges in an image are smeared over a distance of four or five pixels, an effect that is the result of a reasonably sufficient sampling basis, imperfections in the camera optics, and the inevitability of physical laws (finite point spread function). Because edges or features of an image are imaged by the optical and imaging system as continuously varying gray levels, there exists no single gray level that represents edge pixels. For this reason, any system that depends on segmentation or a binary thresholding of the image before critical dimensions are determined must necessarily introduce quantization errors into the measurement. Binary thresholding also exacerbates the resolution limiting effect of system noise. Pixels whose gray levels are close to the threshold level are maximally affected by small variations due to additive noise. They may either be included or excluded into the mask based on the noise contribution to their instantaneous value.

In the prior art methods, an image of the registration mark is sometimes conditioned by linear filtering to reduce artifacts that degrade or prevent accurate measurement. Unfortunately, linear filtering methods are sensitive to the noise surrounding the mark, influencing the position and quality of the edges that are used to determine position. These difficulties are caused by group envelope delay distortion, transient aberration, overshoot, ringing, pre-shoot, phase shift and stored energy within the filter itself caused by extraneous noise surrounding the edge which is the source of measurement information. Additionally, most prior art filters are one dimensional, and cannot take

useful advantage of the marks physical size, known mark structure, mark direction, structural constraints, or basic characteristics that are multidimensional.

Normalized grayscale correlation is used to locate patterns in precise alignment and registration applications. However, the correlation methods are significantly limited when the appearance of objects are subject to change due to normal process variations. Another method of measurement is to filter the image of the registration marks with a linear filter and then to do a gray scale projection of a portion of the mark to produce a one-dimensional portrayal of the transient characteristic of a mark edge that is noise reduced. In the presence of mark rotation from the expected axis, gray scale projection markedly reduces the detected edge amplitude and spreads it over a distance, making thresholding to detect position a very noise sensitive operation. The effects of linear filtering (ringing and transient aberration) cause additional difficulty because these transient errors make thresholding ineffective in determining edge position. Thresholding enshrines the errors that preceded it, forever destroying the ability to make accurate measurements of position and orientation. Using the prior art process, results can be inaccurate when the image of the registration mark is not ideal.

In the prior art, (Serra, Fernando J., "Advanced Search Techniques for Alignment and Registration", Intelligent Vision '99, June 28-29, 1999) recognition of mark characteristics is generally not highly constrained, leading to artifacts and false alarms. Example simple constraints in the prior art include simple edge detection and element length for position location of elements of the composite mark whereas the mark element orientation with respect to other mark elements, edge location all along the length of the mark element, mark size, mark linewidth, etc. could have been used to filter and locate the true mark position. The additional constraints can operate to increase robustness and accuracy for type detection as well as location measurement. Further, they are applied without thresholding where accurate and robust measurements are required.

Objects and Advantages

It is an object of the invention to use a-priori knowledge of registration mark structure in constructing and applying the pre-processing and artifact rejection filter process.

It is an object of the invention to use knowledge of registration mark structure to measure mark position.

It is an object of the invention to generalize the use of a constrained set of marks to actual applications by learning application influences on size, geometry, symmetry, replication, centering and other learnable variations.

It is an object of the invention to use constraints of the registration marking to estimate each registration mark position and to reduce the effects of noise or image rotation.

It is an object of the invention to detect the location, orientation and type of each registration mark by developing distinguishing feature values sequentially.

It is an object of the invention to detect the center of each registration mark based on symmetry of the mark.

It is an object of the invention to use directional elongated filters to preprocess images of registration marks to remove or reduce the effects of noise and imaging artifacts.

It is an object of the invention to further reject artifacts based on symmetry imperfection.

It is an object of the invention to sequentially detect portions of a mark by working systematically outward from the center of the mark.

It is an object of the invention to mask detected portions of the mark while sequentially detecting additional portions of the mark.

It is an object of the invention to classify detected marks to distinguish the actual marks from a set of possible marks.

Summary of the Invention

This invention provides a robust method to find the mark location and determine the type of the mark. Directional elongated filters pre-process the mark image to reject noise and non-mark artifacts and to enhance mark features. Symmetry of the mark is used to further reject non-mark artifacts. The center of the mark is identified based on mark symmetry. Working outward from the center of the mark, sub-portions of the mark are detected and classified in a sequential process. The masks that identify mark location are also used in a later process to direct processing for measuring mark location and orientation.

This invention also provides a robust method to estimate the fine location and angular alignment of marks using the original gray scale image. The mark type classification gives knowledge of appropriate structure. The position, scale, and orientation of the structure associated with the particular mark is the structure used in the structure-guided estimation method. The structure-guided estimation method performs estimation within the regions defined by each component of the measurement mask(s). The structure location is compared to the real data in the image to determine a best fit estimation of mark location and orientation. Estimation finds the parameters that minimize a cost function by a weighted minimum-square-error (MSE) method. This measurement method uses all the pixels associated with the detected portions of the mark to create a measurement having sub-pixel accuracy. Interference with the mark detection and location is minimized by the image pre-processing in the detection process to generate mask and weight information. The masks and a weight image created during the detection process focus the measurement cost function on locations in the image that are

most important and exclude regions that are not related to the measurement. Measurement is not influenced by imperfect initial image orientation.

A learning process is used to incorporate process specific experience into the knowledge of mark structure characteristics and thereby improve noise rejection and image enhancement. Learning improvement enhances the utility of the invention. Learning provides the necessary experience.

Brief Description of the Drawings

The preferred embodiments and other aspects of the invention will become apparent from the following detailed description of the invention when read in conjunction with the accompanying drawings which are provided for the purpose of describing embodiments of the invention and not for limiting same, in which:

Figure 1 shows example registration marks and their axes of symmetry.

Figure 2 shows the block diagram of the overall architecture for detecting and measuring registration marks.

Figure 3 shows the processing flow for the sequential detection of registration marks.

Figure 4 shows the procedure to find the center of the mark and the axes of symmetry.

Figure 5 shows the processing flow for the noise removal procedure.

Figure 6 shows the processing flow for the thresholding step.

Figure 7 shows the processing flow for the artifact removal procedure.

Figure 8 shows an example registration mark;

Figure 9 shows the processing flow for the mark type classification module.

Figure 10 shows a procedure to calculate m(i) where a = 2*s+3 and b = 2*s+1 and s is a learned indication of line width in the registration mark.

Figure 11 shows the procedure for size learning.

Figure 12A shows the processing flow for a noise removal process for horizontal elements of the mark shown in Figure 8.

Figure 12B shows the processing flow for a noise removal process for vertical elements of the mark shown in Figure 8.

Detailed Description of the Invention

1. Concept

Electronic assembly requires automatic alignment of printed circuit board layers before processing steps can be applied. Similarly, a semiconductor manufacturing process requires precise pattern registration among processing layers because the minimum feature size of the pattern is tiny and is getting even smaller. Any registration mismatch between layers has to be detected and corrected. Pre-defined fiducial marks are provided in printed circuit boards for alignment. To detect a semiconductor manufacturing process registration mismatch, registration marks are projected onto each processing layer, and the relative positions of the registration marks between consecutive layers are measured. The alignment or registration check is performed by capturing an image of the printed circuit board or a region of a wafer and searching for pre-defined marks. Once the marks are found, their contours can be extracted and position, scale and orientation of each mark can be determined. Figure 1 shows typical alignment marks for electronic assembly that includes circle 800, ring 820, cross 804, bar 806, triangles 808, 810, diamond 812, or multiple squares 814, 816. Typical registration marks for semiconductor manufacturing processes include rectangular marks such as a box within a box 826, a circle within a circle 820, a ring within a ring 818, or a collection of bars 822 and frames 824. Mark detection and location is a relatively simple task if the marks are well defined and the images have high contrast and low noise. Mark detection can be accomplished by a correlation method. However, in most practical applications, the fiducial marks or registration marks are degraded with low contrast and significant background noise due to the processes they support and normal or occasionally faulty process variations. Furthermore, the types of the marks may not be known. This may require not only detection but also classification of the mark types prior to automatic alignment. This invention provides a robust method to detect difficult-to-locate fiducial marks and classify their types. In one embodiment of the invention, semiconductor wafer processing registration marks are located and their types classified by a structure-guided

method (reference U.S. Patent Application Ser. No. 09/738846 entitled, "Structure-guided Image Processing and Image Feature Enhancement" by Shih-Jong J. Lee, filed December 15, 2000 and U.S. Patent Application Ser. No. 09/739084 entitled, "Structure Guided Image Measurement Method", by Shih-Jong J. Lee et. al., filed December 14, 2000 incorporated herein in their entirety).

2. Overall Architecture

Figure 2 shows the processing flow for the initial and refined estimation of the location of the marks and the determination of the mark type. An input image is received 100. The system detects and coarsely locates the mark 102. The output of the block 102 is a binary image showing the detected mark areas and the mark type. Using the output of the block 102 and the weighted image which is generated by block 104, the finer location of the mark is estimated. The method to get finer mark location is the structure-guided estimation that is a weighted regression method integrating the structures into constraints of estimation. The constraints include geometrical structures such as line, circle, ellipse, and rectangle, and spatial relationships such as parallel, orthogonal, intersection with known angles, or constant distance between lines. The output 106 contains the structure-guided estimation result of the mark that includes the shape structure and the accurate parameters of the structure representation.

2.1 Detection Algorithm

Registration marking systems frequently use more than one mark for alignment of multiple processes or layers or they use a complex mark comprised of portions applied on separate layers. In the preferred embodiment, registration marks are detected one at a time (or one significant portion at a time), generally using a point of symmetry for determination of a working center then working from the center outward (or other systematic method) to detect marks or portions of marks sequentially. In this embodiment, the original image is processed and, working away from the center, the first

mark (or portion of a mark) is detected. The detected portion is masked out of the image. Then begins the detection of mark elements sequentially. The idea in this embodiment is to align marks arising from different processes, or to measure their misalignment. To do this, more than one mark has to be present or the single mark is composed of components that are separately imprinted but form a composite mark. Starting closest to the center and working outward, the first portion of the mark is detected. After completing detection of the first portion of the mark, the first mark portion is excluded from the next sequential mark detection and the process is begun again. This process is repeated until all marks are detected. Finally, the mark type classification is done for each detected mark.

2.1.1. Find the Center and the Symmetry Axes

The method to find the center and the symmetry axes of the registration mark is important for locating and identifying marks of interest. The procedure is illustrated in Figure 4. The first step is a detection module 4102 that detects potential mark features including the true mark and false marks. The output 4103 of module 4102 is the potential mark. The potential mark features are refined using the relation of symmetry and other structure constraints such as equal length, equal shape, line width, or known angles between structures. The center of the mark can be estimated from the centroid or median values of the potential mark. If the measured asymmetry around the estimated center of the mark is not acceptable, then the refinement procedure uses tighter constraint of the relation of symmetry and estimation of the center of the mark is repeated until the measure of asymmetry is acceptable. In a preferred embodiment of the invention, the measure of asymmetry is calculated as

$$\text{Measure of asymmetry} = \Sigma \left([I[x][y] - I[2*Xc - x][2*Yc-y] \right)^2 / \Sigma \left([I[x][y] \right)^2$$

where Xc and Yc are the x and y coordinates of the estimated center of mark, and I[x][y] is the image value at x and y coordinates. The measure is conducted for all points within the potential mark feature images. The measure of asymmetry is zero if the mark features

are symmetric around the center of the mark. The potential marks are not symmetric around the center of the mark if the measure of asymmetry value is high.

As shown in Figure 4, this procedure is performed in module 4104, the output 4105 is the coordinate for the center of the mark.   After the center of the mark is estimated, symmetric axes are estimated.   Let the symmetric axis be

$$a(x-Xc)+b(y-Yc) = 0$$

where $a^2+b^2=1$

and $b>0$.

To find symmetric axis, minimize the following cost function for different values of a and b.

That is: MIN Cost and

                a, b

$$Cost = \Sigma \ ([I[x][y] - I[xr][yr])^2 \ / \ \Sigma \ ([I[x][y])^2$$

where xr and yr is the x and y coordinates of the symmetric point of (x,y) and the relationship is

$$xr = Xc - [2*a*b*(y-Yc) + (a^2 - b^2)*(x-Xc)]$$
$$yr = Yc - [2*a*b*(x-Xc) - (a^2 - b^2)*(y-Yc)]$$

In Figure 4, this operation is done in block 4106, and the output 4107 is the axes of the symmetry.

2.1.2. Sequential Detection of Mark Components

The processing flow for detection of a mark is shown in Figure 3. The mark image contains multiple components of different directional orientation. An elongated directional decomposition is required. After the decomposition, the operations for filtering, detection and artifact rejection are done for the elongate direction associated with that portion of the decomposition. The elongate directions are determined from the information about the mark types that are included in the set of mark possibilities. The processing steps include noise removal and image enhancement 5102, mark segmentation 5104, artifact removal 5106, non-mark object rejection 5108, region exclusion 5112, and mark type classification 5110. The processing parameters such as the direction and size of directional elongated morphological operations are derived from the structure information of the possible marks by determining the basic shapes that make up the mark, the basic size limitations, expected orientation, color, relative orientation to other elements in the mark, width of the basic shapes that constitute the marks, and other distinguishing characteristics. In the example shown in Figure 3, the processing and detection of directionally unique portions of the mark are separately done in parallel, however they can be done serially (e.g. clockwise in addition to from the center out).

The noise removal and image enhancement stage 5102 enhances the image to reduce the effect of noise and enhances features for the detection stage by filtering as described in co-pending U.S. Patent Application Ser. No. 09/738846 entitled, "Structure-guided Image Processing and Image Feature Enhancement" by Shih-Jong J. Lee, filed December 15, 2000 applied to a gray scale image. The mark segmentation stage 5104 thresholds the enhanced image to extract mark areas. The enhanced image may contain noisy or erroneous features, that result in binary artifacts. The artifact removal step removes binary artifacts of the detected mark 5106 by further filtering operations utilizing binary morphological structuring elements and nonlinear morphological operations. The results from each direction of mark processing are collected together. If artifacts remain, they are rejected based upon their symmetry properties. This procedure is done by a rejection stage 5108.

2.1.2.1. Noise Removal and Image Enhancement Step

The noise removal step removes the noise in the image. The noise can be additive noise, spikes, or patterned noise of irrelevant patterns. The noise removal process is accomplished by linear low pass filtering, median filtering, or morphological filtering. In a preferred embodiment of the invention, directional elongated morphological filters are used for noise removal. From the structure of the possible marks, the direction and size of the directional elongated morphological filters can be derived. By chosing the proper structuring element for the feature extraction processing sequence, structure-guided feature extraction can be efficiently accomplished. In a preferred embodiment of this invention, features of different structures are extracted using directional elongated structuring elements. Directional elongated structuring elements have limited width in one of its dimensions. It can be efficiently implemented in a general-purpose computer using the methods taught in co-pending U.S. Patent Applications entitled "U.S. Patent Application Ser. No. 09/693723, "Image Processing System with Enhanced Processing and Memory Management", by Shih-Jong J. Lee et al., filed October 20, 2000 and U.S. Patent Application Ser. No. 09/692948, "High Speed Image Processing Apparatus Using a Cascade of Elongated Filters Programmed in a Computer", by Shih-Jong J. Lee et. al., filed October 20, 2000. The direction of the elongated structuring element is chosen to be approximately orthogonal to the primary direction of the features to be extracted. The process works even if the input edge is slightly rotated. Also, directional elongated filters can be applied on any orientation according to the needs to preprocess for particular mark characteristics. In Figure 5 it is assumed that the mark image has dark elements. If this is not the case, the image can be inverted before pre-processing. The processes of Figure 5 can be applied in any direction and can be reapplied in multiple directions to filter for all expected elements of the mark. In an application, the first directional elongated closing operation 5202 reduces the noise in the mark image by eliminating small dark extraneous image noise. The directional elongated closing residue operation 5204 enhances the mark and brightens the retained elements. The next directional elongated closing operation 5206 fills in the bright image elements. The last directional elongated opening 5208 reduces the noise in the background. The output image of the mark has bright image detail and only contains portions of the mark that are aligned with the

structuring elements in 5202, 5206, 5208. The original image is processed by this method for each principle axis having elements important to the overall mark characteristics.

2.1.2.2. Mark Segmentation

The segmentation step is done by thresholding the output image of the noise removal module 1000 (Figure 6). The thresholding method can be a simple global thresholding or local thresholding based on the neighboring pixel values. The method used in the preferred embodiment is the global thresholding method as shown in Figure 6. In Figure 6, the threshold value is

$$\text{Threshold} = \alpha * \text{maximum pixel value} + (1-\alpha)*\text{median pixel value}$$

α can be any value between 0 and 1 and the maximum and median pixel values are for image pixels 1000 within the operating area of the mask image. The mask input 1002 is the mechanism for region exclusion and indicates the mask output from the previous detection sequence. The delay element 1001 (Figure 3) provides the mask for the previous detection sequence. 0 = blocked, 1 = operating. The input 1000 is the output of the noise removal block 5102 which is a particular directional portion of the mark that is indicated as bright. The output is a segmentation of the remaining portions of the mark (working sequentially for detection) having the filtered direction.

2.1.2.3   Artifact Removal

The artifact removal process 5106 removes thin artifacts caused by noise in the image and/or the detection stage. This output of the detection stage is a binary image with mark elements shown as bright areas. The structuring element is selected to restore the binary image of the portion of the mark that was detected. A general embodiment of the procedure is shown in Figure 7. In Figure 7, the first directional elongated closing operation 5302 restores breaks in the portions of the mark in the mask area that are directionally aligned with the closing operation. The directional elongated opening

operation 5304 is oriented orthogonal to the initial closing operation and the processing element is short enough to preserve mark elements. This closing operation eliminates artifacts that are thinner than the mark, but aligned in a similar direction as the mark. The directional elongated closing operation 5306 closes the double line contained in the particular mark shown in Figure 8 to produce a simpler pattern for measurement.

2.1.2.4   Non-Mark Object Rejection

The non-mark object rejection process 5108 (Figure 3) removes objects that are not qualified as part of the potential mark but are too thick to have been rejected in the artifact removal process 5106.   In the preferred embodiment of the invention, the qualification for an object in a potential mark depends on its symmetry characteristics. For example, the length of the lines located in opposite positions from the center of symmetry should be the same. Since the marks are symmetric along the axis of symmetry, the flipped version of one object along the axis of symmetry should match a counter part object in the received image 5308.   If an object has no matched counterpart in the image 5308, the object is an artifact. In this embodiment, for example, if the symmetric axis is

$$ax+by+c = 0,$$

and the particular object in the output of the artifact removal stage is A. A can be described by its set of pixels, and the counter object is B also described by its set of pixels. Then B is:

$B = \{(xr,yr) \mid (x,y) \in A \}$ and the relationship between (xr,yr) and (x,y) is

$$xr = -2*a*b*y - (a^2 - b^2)*x - 2*a*c$$
$$yr = -2*a*b*x + (a^2 - b^2)*y - 2*a*c$$

In one embodiment of the invention a metric to estimate matching is:

$$\text{matching score} = \text{area}(A \cap B)/\text{area}(A).$$

where $\cap$ indicates the intersection of the two object sets

A larger matching score indicates better matching.

2.1.2.5   Mask Image Region Exclusion

This process 5112 excludes (blocks out) the region(s) of mark(s) that are detected in the current sequence. To simplify the detection process, the portion of the mark detected by the current sequence does not have to be re-detected in the next sequence. This is accomplished in the preferred embodiment of the invention by setting the detected image mask 1002 (Figure 6) values to zero. This excludes the portion of the marks that have already been detected in the current sequence, creating an updated image for detection. The updated image is then used for the detection of remaining marks in the next sequence.

2.1.2.6   Mark Type Classification

The process of mark type classification 5110 is shown in Figure 9. In Figure 9, the first step is feature extraction 5402 followed by the classification step 5404. Because the structures of different mark types are different, the extracted features may include the shape features that span the range of characteristics for the marks that are true possibilities such as the curvature of an arc, direction or intersection angles of lines, and the relations among geometric entities such as the relative position and/or the relative angle between lines and arcs. Other features can be derived from the projection of detected marks to the symmetry axes. Another such useful feature is projection of the filtered image inside the detected portion of the mark in the direction of processing by the

elongated filters. The angle of the symmetric axes can also be a good feature. From these features, the classification step 5404 determines the type of the mark. The classification step can use parametric or non-parametric classifiers to classify the mark.

2.2 Estimate Fine Location Using Intelligent Measurement Method

To estimate finer location of the marks, a structure guided estimation method is used in this embodiment of the invention. In the preferred embodiment, the estimation is performed for inner and outer registration marks separately. The structure guided estimation method of this invention (Reference U.S. Patent Application Ser. No. 09/739084 entitled, "Structure Guided Image Measurement Method", by Shih-Jong J. Lee et. al., filed December 14, 2000 which is incorporated in its entirety herein) is used to estimate the position and orientation of the mark based upon all the detected portions of the mark. This can even be done when portions of the mark are not detected. The detected mark positions (in the respective binary image) mask the locations within the gray scale image that are used to estimate mark position. The position, scale, and orientation of the structure associated with the particular mark is the structure used in the structure-guided estimation method. A weight image may also be used to emphasize particularly important or definitive portions of the mark. The enhanced image output assembled from the outputs 5101, 5103 and 5105 could be used as the weight image if it is desired. The weight image can alternatively be artificially created by the designer. The structure-guided estimation method performs estimation from the weight image within the regions defined by each component of the measurement mask(s). The estimation is conducted by a weighted minimum-square-error (MSE) method. The estimation finds the parameters that minimize a cost function.

$$Cost = \sum_{m \in M} \sum_{i \in C_m} w_i [Model\_Error(x_i, y_i)]^2 - Additional\_structure\_constraint$$

Where M is the set of all components in the measurement masks and C_m corresponds to the m-th component of the mask. Model_Error function represents the difference between the structure representation and the real data. The cost function is defined as the

weighted square error between the structural mark model (symbolic representation) and all data points of all entities included in the estimation minus an additional structural constraint term. Figure 8 shows an example mark that can be used to illustrate the use of constraints. The structure constraint for the outer mark in Figure 8 includes two groups of parallel lines L 300, 306 and P *304, 308*. (The inner mark 302, 310, 312, 314 is separately considered because the purpose in the Figure 8 example is to measure the deviation from assumed position between the two marks. Thus each mark has to be considered separately.) The lines in L and the lines in P are perpendicular to each other as shown in Figure 8. The cost function is

$$Cost = \sum_{m \in L} \sum_{i \in L_m} w_i[a \ x_i + b \ y_i + c_m]^2 +$$

$$\sum_{n \in P} \sum_{i \in P_n} w_i[b \ x_i - a \ y_i + c_n]^2 - \lambda \ (a^2 + b^2 - 1)$$

A closed form solution exists for determining a, b, c_m and c_n that minimize $Cost$.

When P is an empty set, only a parallel line constraint exists for a set of lines. This is a degenerate form of the structure constraint in this more general case. When only one line each existed in groups L and P, the constraint becomes the existence of two orthogonal lines. This is a degenerate form of the structure constraint in this more general case. The entities are defined by each component of the measurement mask(s). The corresponding points in the measurement weight image weigh the data points during the estimation process. The result is very robust to noise and processing anomalies and achieves reliable sub-pixel accuracy.

2.3. Operation Size Determination by Learning

Even for a set of marks with structure constraints that are known a-priori, there are expected variations that can adversely affect performance either for robustness or for measurement accuracy. For best results, the constraints known for each mark type may need to be adjusted somewhat. Where allowance is made for adaptation, we can say that a learning improvement enhances the utility of the invention. Many such adaptations can be learned through a learning process according to the needs of the application. It should not be considered limiting of the scope of the invention that only a single example teaches the art. In the preferred embodiment, the width (size in the direction orthogonal to the elongated direction) of image of the registration mark is not the same for different product designs or manufacturing processing levels. Parameters of the operations are determined through a learning process that involves training for the particular application. If operation size is correctly learned, the registration mark can be more accurately detected because of improved noise removal and image enhancement 5102, thus avoiding a lot of artifacts.

The learning steps for each elongated direction are determined by the following rule:

(1) Set s = 2, m[0] = 0, i = 1

(2) Calculate m[i]

(3) If m[i] < Threshold_Value AND m[i-1] > Threshold_Value, then go to (5) else go to (4)

(4) If s < Max_Size, then s=s+1, i=i+1 go to (2), else go to (5)

(5) Size = 2*s+3

In the above steps, m[i] is the accumulated difference between the results of two different operation size. A small m[i] means the resulting images are almost identical, the Size is the size of the last operation. Note that in this described embodiment, the expected width of mark lines is about 27 pixels. Setting the initial condition s = 2 sets the minimum width of mark lines that can be learned. m[i] is calculated using the process shown in Figure 10 and Figure 11. In Figure 11, the closing operation of size a 5004 and the closing operation of size b 5012 are operated in the orthogonal direction to the direction of the mark

In this embodiment the process of learning mark line width is applicable to many different marks and the principles taught can be applied to features of the marks besides line width. The value Threshold_Value is set to 0.75 as a stop criterion. The Threshold_Value could be set to other values derived from training and selected by the designer. Max_Size is set to13 based in this example on the maximum line width of about 27 pixels. Max_Size could be set to other values derived from training and selected by the designer. The m[i] is calculated using the process shown in Figure 10. Note the explanatory relationship between Figure 11 and Figure 10. Figure 11 explains the processes that are integrated together in Figure 10. The processes 2401, 2402, and 2442 are pre-processing as described in 5002. The upper half of Figure 10 is a process for horizontal line segments and the bottom half of Figure 10 is for vertical line segments. In this particular embodiment, a common part of the pre-processing operation is a 3X3 morphological opening to remove spike noise. The directional elongated processing includes a closing operation of 21X1 to pre-process the horizontal line segment and the 1X21 closing operation to pre-process the vertical line segment. Referring now to Figure 11, we see that pre-processing filters the input learning image of the mark and prepares it for measurement leading eventually to a learned size. Following pre-processing, two different sized closing operations 5004 and 5012 are performed on the pre-processed learning image to compute a difference image between the two results 5011. This difference image is weighted using the closing residue image 5007 through a process of weighting that emphasizes the region of the gray scale image containing the essential information needed to draw conclusions about (in this case) the mark line width (either horizontal, as shown in the top half of Figure 10 or the vertical as shown in the bottom half of Figure 10).

In the preferred embodiment, the closing residue 5007 is used as a weight image for directed measurement of the difference image 5011. The weights allow gray level control of portions of the image according to the importance they represent to the estimation or measurement. This process is described in pending U.S. Patent Application Ser. No. 09/738846 entitled, "Structure-guided Image Processing and Image Feature

Enhancement" by Shih-Jong J. Lee, filed December 15, 2000 which is incorporated in its entirety herein.

The weighted average operation 5010 computes a single value from the weighted difference image:

$$\text{Weighted Average} = \Sigma I[x][y]*Iw[x][y]/\Sigma Iw[x][y]$$

where   Iw[x][y] is the weight image 5007.

I[x][y] is the difference image 5011 between two closing operations performed on the pre-processed image.

The average of the closing residue image 5006 is also computed to normalize for image contrast variability. The normalization occurs through a ratio process 5008 to produce an intermediate result. The resulting m(i) 2416 of operation is the maximum 2414 of the intermediate result values of the operation for horizontal and vertical line segments. Using m(i) the learning process iteration can be completed to converge on a learned line width.

2.4 Noise filtering Using Learned Attribute

Figure 5 shows the general approach to noise filtering using directional elongated filters to pre-process images. This general guidance is combined with the learned attribute in a particular embodiment. For this embodiment, Figure 8 shows the mark type in the image that is pre-processed. In this mark, the lines are all identical width, the principal elements of the mark are oriented generally horizontal or vertical, and the overall mark outer dimensions are greater than 200 pixels. Images of the mark are used in a learning process to determine s, a parameter related to line width. Two pre-processing filters are created as shown in Figure 12A for horizontal structures and Figure 12B for vertical structures. The input image 100 is assumed to be dark lines. If this is not the case, the image is inverted before being applied to these filters. The initial closing with a directional elongated

filtering element 402 cleans up off axis dark noise in the image. The closing residue of the filter 404 uses the learned attribute of line width to clean the mark elements and produces a bright image output. Another directional elongated structuring element is used 406 to fill in the gaps in the bright image of the mark elements followed by another elongated directional structure opening 408 to clean up residual bright noise (dark lines in the original image). The output is a bright image of the lines in the mark that are aligned with the elongated structuring elements of the filter which in this example are horizontal. A complementary process is shown in Figure 12B for the vertical portions of the mark.

The invention has been described herein in considerable detail in order to comply with the Patent Statutes and to provide those skilled in the art with the information needed to apply the novel principles and to construct and use such specialized components as are required. However, it is to be understood that the inventions can be carried out by specifically different equipment and devices, and that various modifications, both as to the equipment details and operating procedures, can be accomplished without departing from the scope of the invention itself.

Advanced Search Techniques for Alignment and Registration

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ABSTRACT

For over a decade, machine vision/automation engineers have relied on normalized grayscale correlation to locate patterns in precision alignment and registration applications. Despite years of commercial success, experienced proponents of this mainstream vision tool are the first to admit that there are certain limitations and caveats associated with its use, especially when the appearance of objects are subject to change due to normal process variations.

Where most of today's traditional pattern locating software tools fall short is in their finicky training processes and in their inability to adapt to normal process variations encountered on the manufacturing floor. These process variations produce such visible phenomenon as non-linear contrast/lighting gradients, contrast reversal, missing or occluded features, blur, artifacts, shadows or reflections, noise, angular or scale changes. Traditional normalized grayscale correlation search tools simply cannot cope with such variations in object appearance.

For OEMs, system integrators and end-users of machine vision requiring highly accurate and robust pattern finding tools capable of precisely locating patterns despite normal process variations, Imaging Technology Incorporated recently introduced a new search technique which overcomes today's pattern matching challenges. The new developments in search algorithms provide extremely accurate and robust pattern locating tool featuring a Training Wizard -- a time-saving utility that takes the guesswork and uncertainty out of the pattern training process. With state-of-the-art geometrical based search engine, the new search tools enables manufacturers of vision-automated equipment to build more robust machines that will automatically adapt to changes in object appearance due to normal process variations.

Keywords: pattern recognition, alignment, adaptive grayscale search, machine vision, robust, automation, process variation

INTRODUCTION

Machine vision has evolved to become a mainstream automation tool enabling computers to replace human vision in high-speed and high-precision manufacturing applications. This important automation tool is now being used to ensure quality in manufacturing of everything from diapers to the chips that power today's most powerful computers.

Some industries have pushed the envelope more than others. In general, those requiring greater levels of precision and robustness have demanded that machine vision tools become more adaptive to in-process variations and provide reliable results despite changes in appearance that can occur during the manufacturing process.

Machine Vision in Industry

The specific industry segments that have pushed the envelope in machine vision are semiconductor and electronics manufacturing. In these segments, machine vision tools are used to precisely guide robotic handling, assembly, and

inspection processes. Some examples of semiconductor manufacturing equipment requiring precision automated alignment include:

- Wafer sorters/handlers/robots
- Ion implanters
- Wafer steppers
- Wafer probers
- Wafer dicers
- Die pick & place
- Die bonders
- Wire bonders
- Dispensers
- Screen printers

What each of these machines has in common is the need to precisely align or register objects such as semiconductor wafers or die so that operations such as lithography, cutting, placing, and/or bonding can be performed to extremely tight tolerances. The machine vision tool that has emerged as the single most important implement for performing precision alignment and registration is the grayscale pattern recognition search or pattern locating tool.
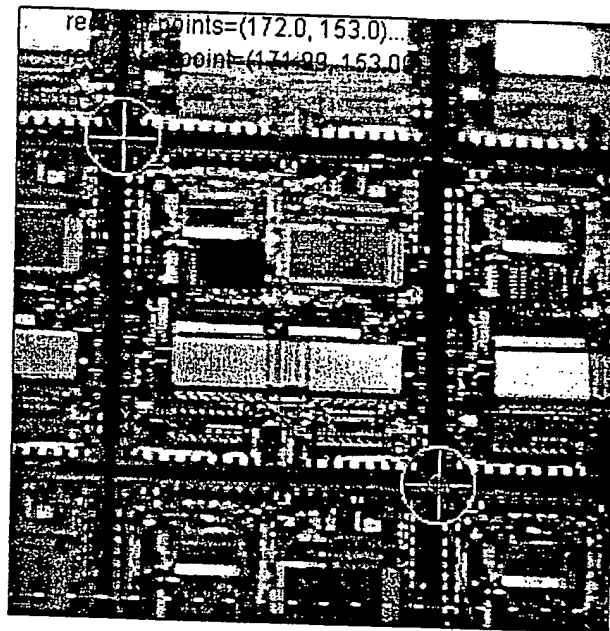
Figure 1. Grayscale pattern recognition used to precisely locate position and orientation of die on a semiconductor wafer for the purpose of controlling a wafer dicer machine as it prepares to cut individual die from the wafer.

The most significant challenge for machine vision in these automated machine applications is maintaining the ability to locate reference patterns despite changes in material appearance. Some examples of changes in visual appearance that can manifest due to normal process variation include:

- Non-linear contrast variation or reversal
- Partial obliteration from debris
- Blur from focus or depth-of-field changes
- Object scale or size deformations
- Angle of orientation

In order to be completely effective in communicating accurate positional data to the alignment and registration process, pattern recognition tools must be able to adapt to the phenomenon listed above.

APPLICATION OVERVIEW

Fine Alignment of Patterned Wafers

Fine alignment of patterned wafers is most commonly the initial process carried out by automated semiconductor and electronics manufacturing machines mentioned in section 1.1 herein. The machine vision tasks are typically as follows:

1. *Determine position of the wafer in the field-of-field (FOV) to within 1 micron in X and Y.*
2. *Determine orientation of the wafer in the field-of-field (FOV) to within 0.1 degree theta.*

The first step in setting up any pattern recognition process is to train the system on the pattern of interest. Unfortunately, the shortcoming of normalized grayscale correlation tools found in most commercial packages today is that once they're trained on a particular pattern, they cannot cope with changes in appearance to the pattern at run-time. Therein lies the limitation of most of today's commercial pattern recognition software packages.

In patterned wafer alignment applications, machine vision challenges are commonly as follows:

Vision Challenge 1: Pattern recognition must handle contrast reversal and non-linear intensity gradients.
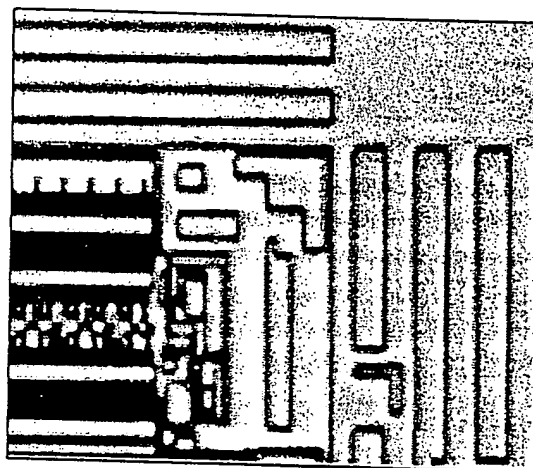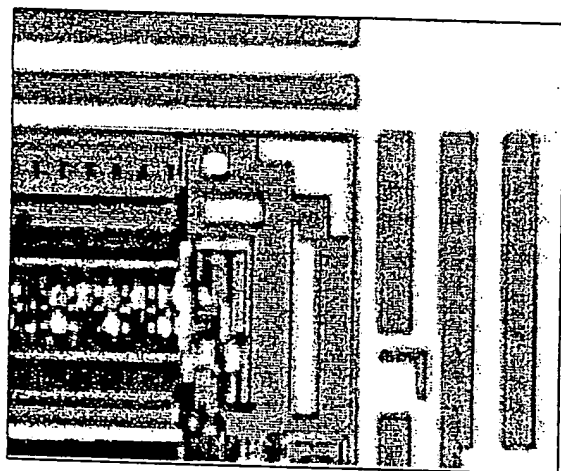
Figure 2. Field-of-view of a semiconductor wafer exhibiting contrast reversal due to Chemical Metal Planarization (CMP) process.

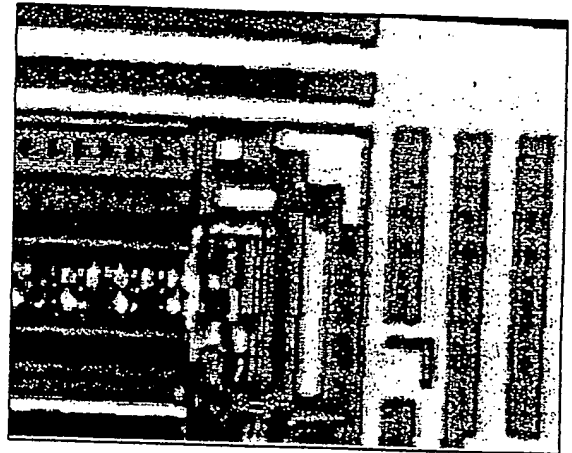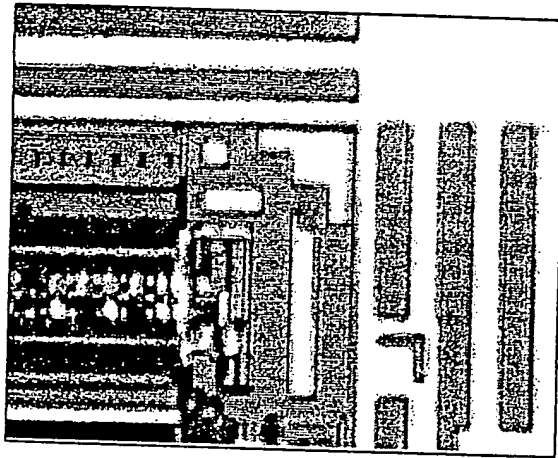Vision Challenge 2: Pattern recognition must handle angle of orientation uncertainties.

Figure 3. Field-of-view of a semiconductor wafer exhibiting changes in angle of orientation due to mechanical positioning uncertainty.

Vision Challenge 3: Pattern recognition must handle blur due to changes in depth-of-field.
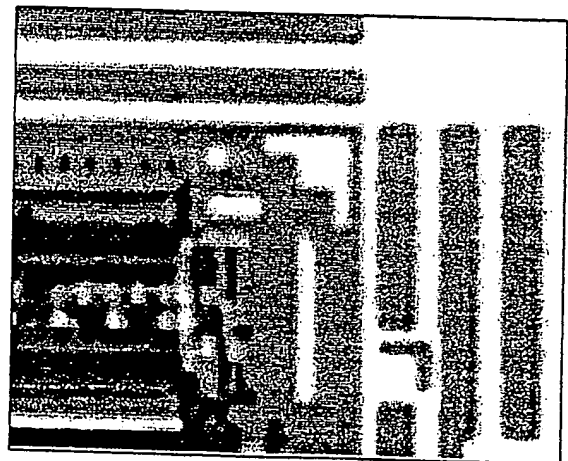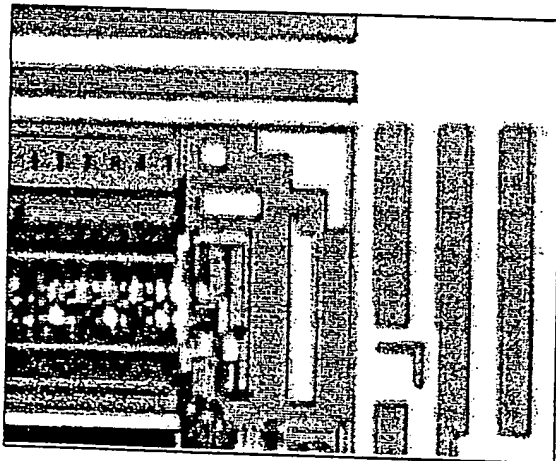

Figure 4. Field-of-view of a semiconductor wafer exhibiting blur due to changes in focus or depth-of-field.

Vision Challenge 4: Pattern recognition must handle changes in scale due to process variation or perspective changes.
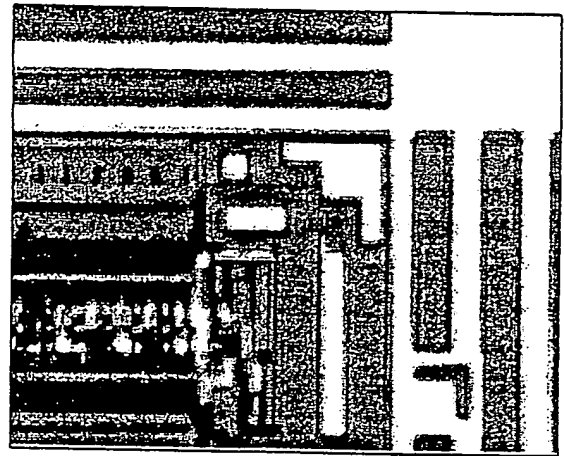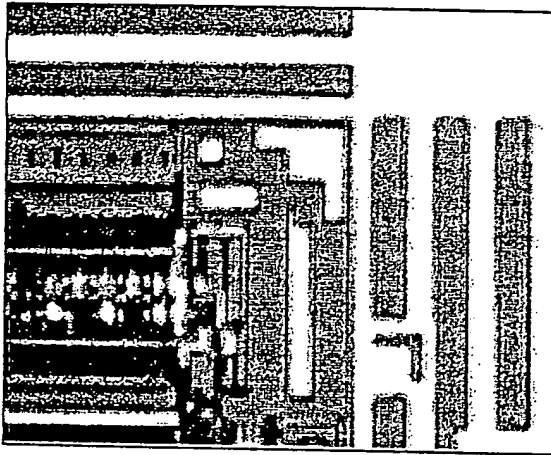

Figure 5. Field-of-view of a semiconductor wafer exhibiting scale changes due to variations in the camera's "Z "positioning.

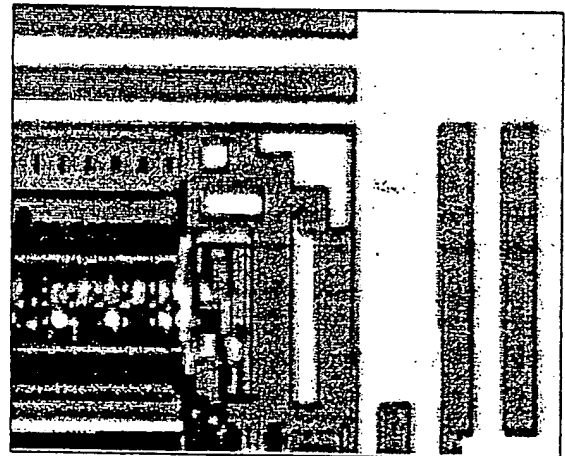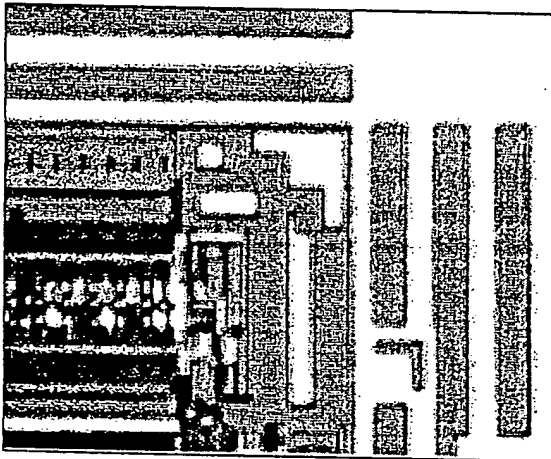Vision Challenge 5: Pattern recognition must handle partial obliteration or missing features.

Figure 6. Field-of-view of a semiconductor wafer exhibiting missing features due to the application of a corrective laser trimming process.

Traditional Machine Vision Approach

Traditional normalized grayscale correlation tools are adequate for locating patterns under ideal conditions. However, they exhibit low tolerance to image changes in scale, angle, blur, obliteration and contrast variation. Also, correlation score resulting from the pattern recognition process is the sole indicator of confidence. Unfortunately, correlation score is sensitive to degraded images and introduces inaccuracies when images are degraded with contrast changes, lighting gradients, scale, obliteration, rotation, and blur.

So, given this common set of requirements for fine alignment of patterned wafers it can be said that standard normalized grayscale correlation tools are essentially rendered ineffective for such applications. From this arises the need for more adaptable pattern recognition tools that can cope with acceptable changes in appearance due to process variations found in today's manufacturing environments.

State-of-the-Art Machine Vision Approach

In order to effectively solve the fine alignment of patterned wafers application an adaptive pattern locating tool is required. Prerequisite characteristics of this tool include:

- Intelligent scoring process for locating patterns
- Report of % in conformance relative to trained pattern
- Report of angle variance from the trained pattern
- Report of contrast variance from the trained pattern
- Precisely locates patterns despite degraded images
- Training Wizard to take the guesswork out of pattern training for worst-case run-time conditions

The new generation of geometric based pattern recognition algorithm is a good example of an adaptive pattern finding tool. This new technique incorporates a highly innovative method that has proven to be extremely reliable in locating the position and orientation of objects despite highly degraded run-time patterns or images. This new tool locates patterns with precision that is 15 times greater than most commonly used tools (i.e., normalized grayscale correlation), and maintains high precision despite changes in object/pattern size, orientation, shape, focus, contrast, or partial occlusion.

Using proven techniques, the new search engine incorporates a geometrical assisted pattern locating tool which automatically adapts to process variations most often encountered during the manufacturing process, while still maintaining up to 1/60th sub-pixel accuracy and repeatability.

To take the guesswork out of the pattern training process, the new search tool includes a Training Wizard which uses proven artificial intelligence techniques to optimize run-time parameters thereby eliminating the need for the traditional "trial and error" efforts mandated by other commercial pattern locating tools. This enables the user to train the vision system on virtually any location or pattern while the Training Wizard automatically computes optimal run-time parameters that enables the search tool to cope with normal process variations that may be encountered at run-time.

SUMMARY

A key element to success in both of the above applications was a pattern recognition tool that is able to locate objects or patterns despite a wide variety of visual variations that occur during run-time. Imaging Technology's new sub-pixel engine is able to accurately locate such patterns to 1/60th pixel accuracy, even when run-time images or patterns exhibit missing features, noise, and random artifacts. In addition, non-linear lighting gradients, including contrast reversal and specular reflections, as well as certain variations in angle and scale are adeptly handled by this new search tool. In situations where only minimal pattern features are visible, This new search engine is still able to precisely locate patterns or fiducials. This robust performance eliminates the need for automation engineers to install expensive handling equipment or costly lighting apparatus.

One of the most important and potentially time-consuming aspects of setting up a vision system is training the pattern recognition tool to work under a variety of process conditions. To eliminate this tedium, this search engine includes an intelligent Training Wizard, which takes hours, if not days of laborious guesswork out of the pattern training process. The Wizard is able to train on any pattern, providing the user with an extremely straightforward "show and go" process. In operation, the Training Wizard creates test suites of templates and images, while evaluating and optimizing search parameters to provide the best possible pattern locating performance at run-time. No other commercially available vision alignment tool on the market provides such an advanced pattern training utility.

This new search technique raises the bar for other vision alignment software tools, providing average search speeds that are up to 5 times faster and 15 times more accurate than competing products. This new level of performance is made possible through industry-proven techniques that were once time-prohibitive for time-critical automated vision applications. Now, with the advent of 450 MHz Pentiums® and MMX® acceleration, these proven techniques are, at last, viable for use in high-speed vision alignment.